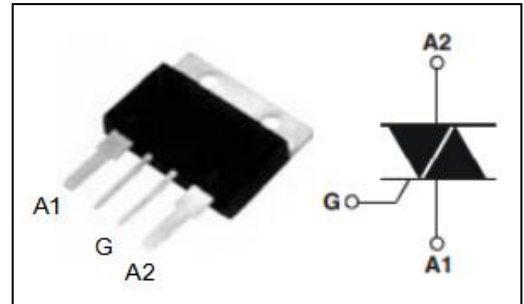


FEATURES

- NPNPN five-layer silicon bidirectional device
- With TO-P4 packaging
- Advanced technology to provide customers with high commutation performances



APPLICATIONS

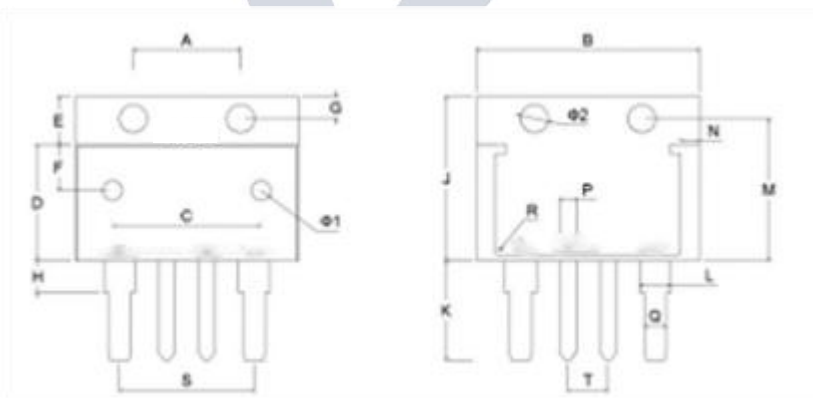
- General purpose motor control circuits
- Home appliances
- Phase control operations in light dimmers and motor speed controllers

ABSOLUTE MAXIMUM RATINGS(Ta=25°C)

SYMBOL	PARAMETER	MIN	UNIT
V _{DRM}	Repetitive peak off-state voltage	800	V
V _{RRM}	Repetitive peak reverse voltage	800	V
I _{T(RSM)}	Average on-state current	80	A
I _{TSM}	Surge non-repetitive on-state current	800	A
T _j	Operating junction temperature	-40~125	°C
T _{stg}	Storage temperature	-40~150	°C

ELECTRICAL CHARACTERISTICS ($T_C=25^{\circ}\text{C}$ unless otherwise specified)

SYMBOL	PARAMETER	CONDITIONS	MAX	UNIT
I_{RRM}	Repetitive peak reverse current	$V_R=V_{RRM}$	1.5	mA
I_{DRM}	Repetitive peak off-state current	$V_D=V_{DRM}$	1.5	mA
I_{GT}	Gate trigger current	I	$V_D=12\text{V}, R_L = 10 \Omega$	mA
		II		
		III		
		IV		
I_H	Holding current	$I_{GT}= 500\text{mA}$	80	mA
V_{GT}	Gate trigger voltage all quadrant	$V_D=12\text{V}, R_L = 10 \Omega$	1.3	V
V_{TM}	On-state voltage	$I_T= 120\text{A}, t_p= 380\mu\text{s}$	1.5	V

PACKAGE OUTLINE
 Dimensions in mm


Dimension	Millimeter	
	Min	Max
A	15.43	16.76
B	32.00	32.30
C	21.30	21.90
D	16.65	17.98
E	6.83	8.06
F	6.60	7.00
G	3.15	3.45
H	4.57	5.44
J	24.06	25.38
K	14.16	15.36
L	4.47	5.23
M	20.76	22.10
N	2.68	3.43
P	2.35	2.77
Q	2.89	3.35
R	1.45	1.55
S	19.95	20.05
T	5.95	6.05
F 1	2.78	4.01
F 2	3.95	5.19

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